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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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of

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Attorney Docket Number

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Application Number

09/821,347

Filing Date

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First Named Inventor:

Kardach

Group Art Unit

2121

Examiner Name

Not yet assigned

## OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner  
Initials\*Cite  
No.<sup>1</sup>

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published

T<sup>2</sup>

CORBAL, J., et al., "DLP+TLP Processors for the Next Generation of Media Workloads", 19-24 Jan. 2001, pages 219-228, High-Performance Computer Architecture, HPCA, The Seventh International Symposium On Monterrey, Mexico.

BURLESON, W., et al., "The Spring Scheduling Coprocessor: A Scheduling Accelerator", March 1999, pages 38-47, Vol. 7, No. 1, IEEE Transactions On Very Large Scale Integration (VLSI) Systems.

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June 25, 04

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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